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2. Strategies to Accelerate Harmonic Minimization in Multilevel Inverters Using a Parallel Genetic Algorithm on Graphical Processing Unit
3. An All-SiC High-Frequency Boost DC-DC Converter Operating at 320 °C Junction Temperature
4. Improved Operation of SiC.BJT-Based Series Resonant Inverter With Optimized Base Drive
5. Novel Time Division Multiple Control Method for Multiple Output Battery Charger
6. Synchronized Zero-Crossing-Based Self-Tuning Capacitor Time-Constant Estimator for Low-Power Digitally Controlled DC-DC Converters
7. Improving High-Frequency Performance of an Input Common Mode EMI Filter Using an Impedance-Mismatching Filter
9. Theoretical Considerations for Single-Phase Interleaved Converters Operated With SHE-PWM
10. Self-Powered Gate Driver for Normally-ON SiC JFETs: Design Considerations and System Limitations
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12. Sensorless Control of Induction-Motor Drive Based on Robust Kalman Filter and Adaptive Speed Estimation.
13. A Three-Phase Current Reconstruction Technique Using Single DC Current Sensor Based on TSPWM.
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16. Digital Sliding-Mode Sensorless Control for Surface-Mounted PMSM.
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27. An Event-Driven Approach to the Current Control of a BLDC Motor Using FPGA.
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32. A New Modulation Method for a 13-Level Asymmetric Inverter Toward Minimum THD.
33. Hybrid Multilevel Inverter Using Switched Capacitor Units.
34. Switching Frequency Derivation for the Cascaded Multilevel Inverter Operating in Current Control Mode Using Multiband Hysteresis Modulation.
35. Experiment and Simulation of a Modular Push–Pull PWM Converter for a Battery Energy Storage System.
38. A Carrier-Based Neutral Voltage Modulation Strategy for Multilevel Cascaded Inverters Under Unbalanced DC Sources.
40. Cascaded Two-Level Inverter-Based Multilevel STATCOM for High-Power Applications.
41. Five-Level Diode-Clamped Inverter With Three-Level Boost Converter.
42. Experimental Verification of a Modular Multilevel Cascade Inverter Based on Double-Star Bridge Cells.
43. A New General Topology for Cascaded Multilevel Inverters With Reduced Number of Components Based on Developed H-Bridge.
44. Analytical Method for Pattern Generation in Five-Level Cascaded H-Bridge Inverter Using Selective Harmonic Elimination.
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64. AC Voltage Sag/Swell Compensator Based on Three-Phase Hybrid Transformer With Buck-Boost Matrix-Reactance Chopper.
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69. Back-Propagation Control Algorithm for Power Quality Improvement Using DSTATCOM.
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74. Multifunctional VSC Controlled Microgrid Using Instantaneous Symmetrical Components Theory.
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81. Universal Digital Controller for Boost CCM Power Factor Correction Stages Based on Current Rebuilding Concept.
84. A Single-Stage Dual-Active-Bridge-Based Soft Switched AC–DC Converter With Open-Loop Power Factor Correction and Other Advanced Features.
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95. Potential of Type-1 Wind Turbines for Assisting With Frequency Support in Storage-Less Diesel Hybrid Mini-Grids.
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112. A Transformerless Grid-Connected Photovoltaic System Based on the Coupled Inductor Single-Stage Boost Three-Phase Inverter.


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127. Closed-Loop Control of DC–DC Dual-Active-Bridge Converters Driving Single-Phase Inverters.


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161. Power Router for Meshed Systems Based on a Fractionally Rated Back-to-Back Converter
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175. Black-Box Model, Identification Technique and Frequency Analysis for PEM Fuel Cell With Overshooted Transient Response
176. ILST Control Algorithm of Single-Stage Dual Purpose Grid Connected Solar PV System
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379. Comparison of the Modular Multilevel DC Converter and the Dual-Active Bridge Converter for Power Conversion in HVDC and MVDC Grids
380. Analysis and Design Considerations of LLCC Resonant Multi-Output Dc/Dc LED Driver with Charge Balancing and Exchanging of Secondary Series Resonant Capacitors
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